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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yvon GRIS
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For: METHOD FOR FABRICATING AN NPN TRANSISTOR
IN A BICMOS TECHNOLOGY

Examiner: Unassigned
Art Unit: Unassigned

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PRELIMINARY AMENDMENT

Prior to examination, please amend this application as follows: